



9/13
C. Barnes
11-13-01

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner of Patents, Washington, DC 20231.

April 27, 2001
Date

Ayesha S. Wilks
Ayesha S. Wilks

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ronnie M. Harrison

Attorney Docket No.: 500395.02

Serial No. : 09/758,970

Group Art Unit : 2819

Filed : January 4, 2001

Examiner : Not Yet Assigned

Title : METHOD AND APPARATUS FOR GENERATING A SEQUENCE OF CLOCK SIGNALS

RECEIVED
MAY - 3 2001
TC 2800 MAIL ROOM

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner of Patents
Washington, D.C. 20231

Sir:

Please amend the above-identified application as follows:

In the Claims:

Please ~~cancel~~ claim 1.

Please add new claims 57-90 as follows:

--57. (New) A method of generating a sequence of clock signals, comprising:
delay locking a reference clock signal to a master clock signal so that the
reference clock signal has a predetermined phase relative to the phase of the master clock signal;
and

05/01/2001 EABUBAK1 00000019 09758970

01 FC:102
02 FC:103

160.00 OP
234.00 OP